



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PRPOFILE

Name of the Faculty	I.Pavani Prapurna
Designation	Assistant Professor
Email & Phone No	pavaniinapanuri@gmail.com & 8297938333
Department	Electronics and Communication Engineering
Educational Background	1. M.Tech From Vardhaman College of Engineering (JNTUH), Shamshabad. 2. B.Tech From SBIT (JNTUH), Khammam.
Research Areas	-
Total Experience	13 Years
Teaching/ Research Experience	13 Years



Sl. No	Institute	Designation	Period
3	Dhanekula Institute of Engineering & Technology	Asst.Prof	2014 - till date
2	Malla Reddy Engineering College for Women	Asst.Prof	2011 - 2014
1	Vardhaman College of Engineering	Asst.Prof	2009 - 2011

List of Publications (National and International Journals)

- Published a paper in International Research Journal of Engineering and Technology (IRJET) with paper entitled "Speech and Face Recognition Based Locker Security System", 2020.

National/International Conferences ((National and International)

- Published a paper in the NATIONAL CONFERENCE on SPCOM-14 with paper entitled "Spatial Domain Invisible Watermarking for Color Images Based on Spread Spectrum".

2. Published a paper in the NATIONAL CONFERENCE with paper entitled “Design of Embedded Web Server for ECG Measurement in Medical Applications”.
3. Published a paper in the NATIONAL CONFERENCE on SPCOM-10 with paper entitled “Spread Spectrum Based Spatial Domain Invisible Watermarking Algorithm for Still Images”.

Achievements / Awards etc.,

- A certificate of Appreciation in NATIONAL level E-QUIZ on “Fundamentals Of Electronics” conducted by NSRIT in association with MHRDs INNOVATION CELL.
- A certificate of Appreciation in Online Quiz on “Fundamentals of VLSI Design” conducted by Dept.of ECE, Shadan Women’s college of Engineering and Technology, Hyderabad, 2020.
- A certificate of Achievement in Faculty Skill Test on “Digital Electronics” conducted by Dept. of ECE,DIET.
- Organized in Faculty Development Program on “Analog Systems Design Using ASLK PRO” in Dept.of ECE of DIET,2016.
- Ratified as Asst. Professor by JNTUK and JNTUH.
- Qualified in Faculty Eligibility Test Conducted By JNTUH.

I.PAVANI PRAPURNA