

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY (Approved by AICTE Affiliated to JNTU Kakinada)

Ganguru, Vijayawada – 521 131. Phone: 0866 – 2583842 / 43. Email: dietoffice2009@rediffmail.com; URL: www.diet.ac.in

FACULTY PROFILE

Name of the Faculty

Naga Prasanthi Yerneni

Designation Assistant Professor

Email & Phone No nagaprasanthi@diet.ac.in & 9441786778

Department Electronics and communications Engineering

Educational Background Registered PhD Program in KL Deemed University on Jan 2021

M.Tech From JNTU, Kakinada affiliated college with 79.64%

B. Tech From JNTU, Kakinada affiliated college with 70.14%

Intermediate with 89%

SSC with 86.6%

Specialization Embedded Systems

Experience 11 Years

Industrial Experience Nil

Sl. No	Institute	Designation	Period
1	Dhanekula Institute of Engineering	Asst.Prof	2013 - till date
	and Technology		
2	QIS Institute of technology, Ongole	Asst.Prof	2012 - 2013

Research area:

Infrared Non-Destructive Testing, Signal Processing through Deep Learning

Subjects Taught:

- Switching Theory and Logic Design
- Pulse and Digital Circuits
- Linear IC Application
- Linear and Digital IC applications
- Microprocessors and Microcontrollers
- Professional Ethics and Human Recourses
- Computer Organization and Architecture
- Electronic Switching systems
- Embedded systems
- Introduction to Machine Learning

Analog Communications

Laboratories Handled:

- Linear IC Applications lab
- Pulse and digital circuits lab
- Microprocessors and Microcontroller lab
- Digital Signal Processing Lab
- Digital Communications Lab

List of Publications

- Yerneni, Naga Prasanthi, V. S. Ghali, and G. T. Vesala. "Feature Recognition in Quadratic Frequency Modulated Thermal Wave Imaging for Subsurface Defect Detection in Fiber-Reinforced Polymers." *Russian Journal of Nondestructive Testing* 59, no. 11 (2023): 1177-1190.
- Vesala, G. T., V. S. Ghali, S. Subhani, and Y. Naga Prasanthi. "Material characterisation by enhanced resolution in non-stationary thermal wave imaging." *Insight-Non-Destructive Testing and Condition Monitoring* 63, no. 12 (2021): 721-726.

Conference Proceedings

• **Prasanthi, Y. Naga**, V. S. Ghali, G. T. Vesala, and B. Suresh. "Convolutional neural network for automatic defect detection in composites." In *AIP Conference Proceedings*, vol. 2512, no. 1. AIP Publishing, 2024.

National/International Conferences

- Participated in three-day international conference on "Non-Destructive Evaluation" during 24-12-2022 to 26-12-2022 organized by Indian Society for Non-Destructive Testing, Gandhinagar, Gujarat. "Unsupervised Deep Learning in Infrared Thermography for Automatic Defect Detection".
- International Conference on Signal Processing & Communication Engineering Systems
 Organized by Department of Electronics and Communication Engineering, Koneru Lakshmaiah
 Education Foundation (Deemed to be University) Vaddeswaram, Guntur (Dist.), A.P India, 11th to 12th June 21 "Convolutional Neural Network for Automatic Defect Detection in
 Composites."
- Attended INTERNATIONAL CONFERENCE on **Allied Electrical and Communication Systems** 8-10 December 2016.

Faculty Development Programs/Workshops/STTPs

• Participated in the AICTE Recognized Faculty Development Programme on "Antenna Design Techniques and Tools" Conducted by Electronics and communications department from 11/09/2023 to 15/09/2023 (One Week) at Dhanekula institute of engineering and technology in association with NITTTR, Chandigarh.

- Attended one week National level Faculty Development Program on "**Recent advances in Electronics and communications engineering** –**An approach through AI and ML**" organized by Vignan's Nirula Institute Of Technology And Science For Women, Guntur, AP held during 21 st to 26th Aug 2023.
- Participated in Five day **Faculty development program on "Foundations of Data Structures Using Java"** from 12/06/2023 to 16/06/2023 at Dhanekula Institute of Engineering and Technology, Vijayawada, Ganguru.
- Participated in Three-day hands-on training (FDP) on "VLSI Training on Symica EDA tool" from 17/04/2023 to 19/04/2023 at Dhanekula Institute of Engineering and Technology, Vijayawada, Ganguru.
- Participated in six-day online FDP on "Hands-on Deep Learning Architectures for Data Classification Using Python" during 26-09-2022 to 01-10-2022 organized by DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY, Ganguru, Vijayawada in association with AIQUANTUM Smart Solutions Pvt. Ltd, STEP, NITK Surathkal.
- Participated in the AICTE Recognized Faculty Development Programme on Nano sensors & Devices Conducted by Applied Science Department from 22/08/2022 to 26/08/2022 (One Week) at Dhanekula Institute of Engineering and Technology, Ganguru, Vijayawada, Andhra Pradesh.
- Participated in the AICTE Recognized Faculty Development Programme on **Machine Learning** and **Predictive Analysis using Python** Conducted by Computer Science and Engineering Department from 11/07/2022 to 15/07/2022 (One Week) at NITTTR, Chandigarh.
- Participated in the AICTE Recognized Faculty Development Programme on Preparing Students for Job Interviews Conducted by Education and Educational Management Department From 18/07/2022 to 22/07/2022 (One Week) at NITTTR, Chandigarh.
- Attended Five-day online Faculty Development Program on "Advances in Signal Processing", organized by Department of Electronics & Communication Engineering, Lakireddy Bali Reddy College of Engineering, Mylavaram, Krishna District held during 04.04.2022 to 08.04.2022.
- Attended one-week online STTP on "Internet of Things (IOT) Machine Learning (ML)"
 Organized by Department of Electronics and communications Engineering GPREC, Kurnool during 20/09/2021 and 25/09/2021.
- Attended International Workshop on "Signal Processing Using Machine Learning Applications & Hands-On (Online)" Technically Sponsored by IEEE Guntur Subsection, Date: 28th August to 1st September 2021 (Saturday to Wednesday)
- Attended FDP on "**Recent Trends in Next Generation Communication Technologies**" from 16th-20th August,2021 organized by BAPATLA college of engineering
- Attended STTP on APPLIED MACHINE LEARNING ALGORITHMS AND APPLICATIONS (PHASE-III) from 14th to 19th December 2020 organized by Annamacharya institute of Technology and Sciences: Tirupati
- Attended FDP on **APPLIED MACHINE LEARNING AND DEEP LEARNING** from 15th June to 20th June 2020 Organized by APSSDC

- Conducted a faculty development program on ASLK kits which are sponsored by Texas Instruments in 2017 at our ECE Department, Dhanekula institute of Engineering and technology.
- Attended FDP for three days on **Custom IC design and ASIC Implementation** using cadence EDA Tools from on 3rd ,4th, and 5th August 2016.
- Attended FDP for two days on **Analog system design using ASLK Pro** from 16th and 17th Feb. 2016.
- Attended Embedded workshop conducted in Dhanekula Institute of Engineering and Technology for the academic year 2014-15
- Attended Advanced Trends in Embedded Systems, ATES 2014 ON 28TH Feb 1st March 2014, Conducted by PVPSIT, Kanuru, Vijayawada.

Professional Development programs:

Attended Five-day online Professional Development program on "REFLECTIVE TEACHING
FOR EFFECTIVE TEACHERS "conducted by the National institute of technical teachers
training and research (NITTR), Ministry of Education, Government of India Chennai held
during 25.04.2022 to 29.04.2022.

Achievements / Awards:

• Received certificate of Appreciation as **BEST MENTOR** for the academic year 2017-18 from Department of ECE.

Certification courses:

- Successfully completed a course "Basics of AI and ML" on Infosys springboard Platform.
- Successfully completed a course "Basics of Python "on Infosys springboard Platform.
- AI For Everyone on Coursera Platform
- Introduction to Electronics on Coursera Platform
- Introduction to the Internet of Things and Embedded Systems on Coursera Platform.

Declaration:

I hereby declare that the information furnished above is true to the best of my knowledge.

Place:	Naga Prasanthi Yerneni
Date:	