

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY
Department of Electronics & Communication Engineering



DIET/7.5.1/FT 12A

UG TIME TABLE

Name of the Program: B. Tech in ECE

Class/Sem: III B. Tech SEM-II

Sec: B

Class Room No: F -43

A. Y: 2023-24

w.e. f: 01.01.2024

DAY	1 9:00-9:50	2 9:50-10:40	3 10:50-11:30	4 11:30-12:20	5 12:20-1:10	LUNCH	6 2:00-2:50	7 2:50-3:40	8 3:40-4:30
MON	CN	VLSID	MPMC /DSP Lab				MPMC	MCC	DSP
TUE	DSP	MPMC	VLSI /ARM lab				CN	RM	SPORTS
WED	MCC	VLSID	ITC				ARM/ VLSI lab		
THU	CN	MCC	VLSID	VLSID	DSP		MCC	CN	LIB
FRI	DSP	MPMC	DSP / MPMC Lab				Project		
SAT	MPMC	DSP	MCC	MPMC	VLSID		RM	COUN	CN

Theory/Lab	Faculty Name	L	T	P	O	Sign
Microprocessor and Microcontrollers	Mrs Y.N.Prasanthi	4	1	--	--	
VLSI Design	Mr S.Chandrasekhar	4	1	--	--	
Digital Signal Processing	Dr. L.Naga Srinivas	4	1	--	--	
Mobile & Cellular Communication (PE -2)	Mr P.Veera Swamy	4	1	--	--	
Computer networks (OE-2)	Dr Surendra Loya	4	1	--	--	
Research Methodology	Mr Y.Rama Krishna	2	--	--	--	
Microprocessor and Microcontrollers - Lab	Mrs Y.N.Prasanthi	--	--	3	--	
VLSI Design Lab	Mr S.Krishna	--	--	3	--	
Digital Signal Processing Lab	Mrs I.Pavani Prapurna	--	--	3	--	
ARM based/ Aurdino based Programming lab	Mrs M.Durga Bhavani	--	--	3	--	
ITC (EEE simulation lab)	Mr Md. Abdul Aziz	--	--	--	3	
Project	Dr K.Srinivas Rao/Dr Pavithra roy	--	--	--	3	
Sports	Mrs M.Durga Bhavani	--	--	--	1	
Library	Mr Ch.Ramesh Babu	--	--	--	1	
Counseling	S.K./Y.NP/Y. RK	--	--	--	1	
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Class In charge		Mr S.Krishna				

Y. Naga
 Assistant Professor
 Department of Electronics
 & Communication Engineering
DHANEKULA INSTITUTE OF ENGINEERING AND TECHNOLOGY
 Ganguru, VIJAYAWADA-521 139

C. Das
 HOD / Dept
HEAD OF THE DEPARTMENT
 Electronics & Communication Engineering
DHANEKULA INSTITUTE OF ENGINEERING AND TECHNOLOGY
 Ganguru, VIJAYAWADA-521 139

N. Lakshmi
 Principal
Principal
DHANEKULA INSTITUTE OF ENGINEERING AND TECHNOLOGY
 Ganguru, Vijayawada-521, 139