



DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PROFILE



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|------------------------------|--|
| Name of the Faculty | Lakshmi Maganti |
| Designation | Assistant Professor |
| Date of Joining | 05-07-2023 |
| Nature of Association | Regular |
| Email & Phone No | lakshmiijn.maganti@gmail.com 9490735969 |
| Department | Electronics and Communication Engineering |

Educational Background

1. M.Tech from Gayatri Vidya Parishad Engineering College, Visakhapatnam
2. B.Tech(ECE) from D.M.S.S.V.H college of Engineering, Machilipatnam

Areas of Specialization - VLSI Design and Embedded Systems

Total Experience - 1 years 6 months

Teaching/Research Months Experience. 1 years 6 months

| Sl.No | Institute | Designation | Period |
|-------|---|-------------|----------------------|
| 1 | D.M.S.S.V.H college of Engineering | Asst.Prof | July 2022-June 2023 |
| 2 | DhaneKula institute of engineering and technology | Asst.Prof | July 2023- till date |

List of Publications (International Journals)-

1. M.Lakshmi, K. Naresh Kumar Sagara Pandu" Analysis and Implementation of Feed Through Logic for High Speed and Low Power Structures" International Journal of Computer Applications (0975 - 8887) Volume 82 - No 18, pp 29-31, November 2013

Faculty Development Program/Workshops:

1. Attended one week Workshop on “VLSI to System Design: Silicon to end Application Approach”, organized by AICTE, ARM Education and STMicroelectronics from 31.07.2023 to 04.08.2023.
2. Participated in the AICTE Training And Learning (ATAL) Academy Faculty Development Program on “Recent Advancements in VLSI Design and Fabrication”: From Theory to Practice at DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY from 04/12/2023 to 09/12/2023.
3. Attended One week online Faculty Development Program on “Enhancing expertise: Communication and Signal/Image Processing”, organized by Dept of ECE ,Aditya college of Engineering and Technology, held during 10.07.2023 to 15.07.2023.
4. Attended 3 day National level Faculty Development Program on “Emerging Trends on VLSI Device, Circuit and System Design”, organized by Dept of ECE , RVR & JC college of Engineering , held during 09.11.2023 to 11.11.2023

Lakshmi Maganti