

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE, Affiliated to JNTUK Kakinada)

Programmes Accredited by NBA: B.Tech in CSE, MEC, EEE & ECE

NAAC Accredited & An ISO 9001-2015 Certified Institution

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Faculty Profile

Name of faculty Mr. Chandra Sekhar Savalam
Designation Assistant Professor
Department Electronics and Communication Engineering

Date of Joining 01.11.2012



Nature of Association Regular
Email csekhar.savalam@gmail.com, css@diet.ac.in
Phone Number +91 94404 64408
Educational Background Pursuing Ph.D. (VLSI Design), JNTUK University, Kakinada

M.Tech (VLSI SD), JNTUK University, Kakinada
B.Tech (ECE), JNTUH University, Hyderabad

Total Experience 16 Years

Industrial

Experience -

Teaching Experience 16 Years

Area of

Specialization VLSI Design

Research Areas Custom IC Design, Fault-Tolerant Designs Using ECC, FPGA Design and Signal Processing in VLSI.

Experience:			
S.No	Institute/Industry	Designation	Period
1.	Dhanekula Institute of Engineering & Technology	Asst.Professor	1.11.2012 to till date
2.	MVR College of Engineering and Technology, Paritala. Vijayawada.	Asst.Professor	08-07-2010 to 17-10-2012
3.	Raja Mahendra College of Engineering and Technology, Hydrabad.	Asst.Professor	07-11-2007 to 13-10-2009
4.	Anwarul uloom College of Engineering and Technology, Vikarabad.	Asst.Professor	05-06-2006 to 31-10-2007

List of Publications (National and International Journals)	
1	Savalam, C., Alapati, V.N.T. Soft error detection and correction for parallel digital filters using Hamming code. <i>Int J Syst Assur Eng Manag</i> (2023). https://doi.org/10.1007/s13198-023-01876-6 (ESCI,Scopus)
2	Savalam, C., Alapati, V.N.T. Digital twin based FPGA implementation of FIR filter for multi-bit soft computing error detection and correction for industrial applications. <i>Soft Comput</i> 27, 4289–4306 (2023). https://doi.org/10.1007/s00500-022-07371-7 (SCIE)
3	Korapati Prasanti, S. Kalpana, Chandra Sekhar Savalam, “Protection of Cultivated Fields during Heavy Rains and Air” <i>Journal of Green Engineering</i> , Volume-10, Issue-12, December 2020, Page No.13311-13320. (Scopus)
4	Chandrasekhar Savalam , Shaheda Sultana, MD.Tarunam Jahan , N.Durga Prasad , P.Komali, “Design and Implementation of Reverse Carry Propagate Adder(RCPA) on FPGA” <i>International Journal of Innovative Engineering and Management Research (IJIEMR)</i> , Vol 09 Issue07, July 2020.
5	S.Chandra Sekhar, Prasanti Korapati, “Design and Analysis of Gray to Binary converter using Quantum Dot Cellular Automata(QCA)” <i>Test Engineering and Management</i> , March-April 2020 ISSN: 0193-4120 Page No. 13018 – 13023
6	S.Chandra Sekhar, Prasanti Korapati, AS Haranadh, “ Design and Implementation of High performance and low power Mixed logic line Decoders” <i>International Journal of Innovative Technology and Exploring Engineering (IJITEE)</i> ISSN: 2278-3075, Volume-8, Issue- 6S4, April 2019. (Scopus)
7	D.Yamini , S.Chandra Sekhar, “10T SRAM-VDD pre-charge using read port for low switching power and low RBL leakage” <i>International Journal of Scientific Development and Research (IJSDR)</i> , Volume 5 Issue 15 May 2018. e-ISSN: 2348-6848, p-I ISSN: 2348-795X.
8	P Divya, S.Chandra Sekhar, “Carry skip adder (CSKA) high-speed operating under wide range of supply logic at different levels” <i>International Journal of Scientific Development and Research (IJSDR)</i> , Volume 3 Issue 4 April 2018. ISSN: 2455-2631.
9	CH. Harika, S.Chandra Shekhar, “Systematic Error Correcting Codes Implementation for Matching of Data Encoded” <i>International Journal of Professional Engineering Studies (IJPRES)</i> , Volume VIII Issue 2 ,January 2017.
10	T.Sikhamani, Chandrasekhar Savalam, “Design of Fault Tolerant Parallel FFT’s Using Error Correction Codes and Parseval Checks” <i>International Journal & Magazine of Engineering, Technology, Management and Research (IJMETMR)</i> , Volume 4 Issue 5 May 2017. ISSN No: 2348-4845.
11	G.Janani , S.Chandra Sekhar, “Direct Design of Reversible Combinational and Sequential Circuits Using PSDRM Expressions” <i>International Journal of Research in Engineering and Science (IJRES)</i> , Volume 3 Issue 11, November. 2015. ISSN (Online): 2320-9364, ISSN (Print): 2320-9356.
12	S.Chandra Sekhar, J.Lakshmi Prasanna, “128 Bit Parallel Prefix Tree Structure Comparator” <i>International Journal of Research in Engineering and Science (IJRES)</i> , Volume 3 Issue 10, October. 2015. ISSN (Online): 2320-9364, ISSN (Print): 2320-9356.
13	Chandrasekhar Savalam, Prasanti Korapati, “Implementation and Design of AES S-Box on FPGA” <i>International Journal of Research in Engineering and Science (IJRES)</i> , Volume 3 Issue 1, Jan. 2015. ISSN (Online): 2320-9364, ISSN (Print): 2320-9356.

National/International Conferences (National and International)	
1.	P. Korapati, C. S. Savalam, C. Vailipalli and A. Pingali, "Charging of Wearable Device Using Human Warmth," 2023 International Conference on Communication, Circuits, and Systems (IC3S), BHUBANESWAR, India, 2023, pp. 1-5, doi: 10.1109/IC3S57698.2023.10169660.
2.	Korapati Prasanti, Kalpana Seelam, Ch Jayalakshmi, Chandra Sekhar Savalam “Preventive system for forests property using wireless communication” in IEEE 2019 5th International Conference on Advanced Computing & Communication Systems (ICACCS), JCT Institutions Engineering and polytechnic, Pichanur, COIMBATORE, TAMIL NADU, INDIA

3.	K Prasanti, S Kalpana, Chandra Sekhar Savalam "A Novel Feature of Increased Safety During Car Crashes" in IEEE 2 nd International Conference on Inventive Systems and Control (ICISC 2018) organized during 19 th and 20 th January, 2018, JCT Institutions Engineering and polytechnic, Pichanur, COIMBATORE, TAMIL NADU, INDIA
4	K Prasanti, Ch JayaLakshmi, S.Chandra Sekhar "A prototype controlling of barrage gates using Arduino under flood conditions" in (IET&IEEE) 3 rd International conference on Electrical, Electronics, Engineering Trends, Communication, Optimization and Sciences(EEECOS-2016) organized by SASI Institute of Technology & Engineering, Tadepalligudem, Westgodavari District, Andhrapradesh, India, During 1 st to 2 nd , June 2016.
5.	Ch.Jaya Lakshmi, K.Prasanti, S.Chandra Sekhar "Performance Analysis of Image Fusion for Different Wavelet Families" in (IEEE) March 2016 International Conference on Innovations in information Embedded and Communication Systems (ICIIECS'16) organized during March 2016, Karpagam College of Engineering, Coimbatore, COIMBATORE, TAMIL NADU, INDIA.

List of Workshops/Seminars/Guest lectures/FDPs/Webinars- Attended	
1	Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy Offline Elementary FDP on " <i>Recent Advancements in VLSI Design and Fabrication: From Theory to Practice</i> " from 04-12-2023 to 09-12-2023 at <i>DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY</i> .
	<i>Participated in the AICTE Recognized Faculty Development Programme on Antenna Design Techniques and Tools Conducted by Development of ECE from 11/09/2023 to 15/09/2023 (One Week) at Dhanekula Institute of Engineering and Technology Ganguru Vijayawada, Krishna.</i>
	Actively participated & completed successfully a three-day hands-on training (FDP) on " VLSI Design Using Symica EDA Tool" from 17/04/2023 to 19/04/2023 at Dhanekula Institute of Engineering and Technology, Ganguru.
2	Actively <i>participated in the</i> Faculty Development Program on Drones & Autonomous Systems organized by the Center of Excellence Drones, CMR Institute of Technology, Bengaluru in association with IEEE CMRIT Student Branch from 02-05-2023 to 06-05-2023.
3	<i>Participated in the AICTE Recognized Faculty Development Programme on Curriculum Design for Developing Job related Competencies Conducted by Curriculum Development Centre Department from 09/01/2023 to 13/01/2023 (One Week) at Dhanekula Institute of Engineering and Technology Ganguru Vijayawada, Krishna.</i>
4	Actively participated in the Faculty Development Programme " <i>VLSI Exposure and it's Challenges in Engineering</i> " Organized by Department of Electronics & Communication Engineering, Krishna University College of Engineering and Technology, Krishna University Conducted from 11th July 2022 to 15th July 2022.
5	<i>Participated in the AICTE Recognized Faculty Development Programme on Nanosensors & Devices Conducted by Applied Science Department from 22/08/2022 to 26/08/2022 (One Week) at Dhanekula Institute of Engineering and Technology, Ganguru, Vijayawada, Andhra Pradesh, Krishna.</i>
6	Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy Online Elementary FDP on "Custom IC Design in the Advanced CMOS Technology" from 21-11-2021 to 25-11-2021 at Indian Institute of Information Technology Pune.
7	participated & completed successfully AICTE Training And Learning (ATAL) Academy Online Elementary FDP on "VLSI Design Techniques and its Applications in AI/ML" from 15-11-2021 to 19-11-2021 at Thapar Institute of Engineering & Technology, Patiala.
8	Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Lab on Chip" from 2021-1-18 to 2021-1-22 at S.R. M INSTITUTE OF SCIENCE AND TECHNOLOGY.
9	Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Sensors Technology" from 2021-1-4 to 2021-1-8 at S.R. M INSTITUTE OF SCIENCE AND TECHNOLOGY.
10	Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Micro-electromechanical Systems" from 2020-11-23 to 2020-11-27 at SARDAR PATEL COLLEGE OF ENGINEERING.

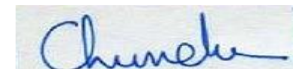
11	Actively participated & completed successfully AICTE Training And Learning (ATAL) Academy Online FDP on "Internet of Things (IoT)" from 2020-11-14 to 2020-11-18 at Indian Institute of Technology Goa (IIT Goa).
12	Actively participated in AICTE sponsored short term training program on "Trends and Challenges in Medical Image Analysis Through Deep Learning Algorithms" Phase-I, Organised by Department of Electronics and Instrumentation Engineering from 26 th October to 31 st October, 2020.
13	Attended "A 3-Day Online Certification Program on Designing FPGA's Using the Vivado Design Suite" held from 8 th June to 10 th June 2020, by sandeepani school of Embedded System Design, Bangalore in association with CoreEL Technologies and Xilinx.
14	Attended "A One Week National Level Online Faculty Development Program on Innovation to Academics" organized by the Ramachandra College of Engineering, Eluru, in association with National Science & Technology Entrepreneurship Development Board (NSTEDB) Conducted from 11-05-2020 to 16-05-2020.
15	Attended "A 4-Day Online FDP on Data Science and Machine Learning using Python" organized by the Department of Electronics and Computer Engineering, K L University held on 20th-23rd April 2020.
16	Attended a Webinar on "Challenges and Importance of e-Waste management", organized by the IEEE Communications Society Students chapter, Department of Electronics and Communication Engineering, Sreenidhi Institute of Science and Technology, Hyderabad in association with IEEE product Safety Engineering Society, India through Online.
17	Participated in the International Webinar on "Nano-Structured thin films for Multifunctional Applications" organised by the Departments of Chemistry, Physics & Electronics, on 11th June 2020.
18	Attended "A Webinar on Artificial Intelligence & Machine Learning-Challenges, Opportunities and Emerging Applications" organized by the Department of Electronics and Computer Engineering, K L University held on 30th April 2020.
19	Attended "A Webinar on Machine Learning Techniques for detection and classification" organized by the Department of Electronics and Computer Engineering, K L University held on 25th April 2020.
20	Attended "A Webinar on Examination reforms to complement OBE requirements" organized by the Department of Electronics and Computer Engineering, K L University held on 24th April 2020.
21	Attended the Webinar on "Variable Frequency Drives – Cutting Edge Technology" organized by Department of Electronics and Communication Engineering, Easwari Engineering College on 14th May, 2020.
22	Attended an International Webinar on Microfabrication Technology organized by the Dept. of Computer Science and Engineering, JIS College of Engineering, Kalyani, Nadia, West Bengal, India, on the 9 th of July 2020.
23	Attended an International Webinar on Time Series Analysis, Modeling and Forecasting organized by the Dept. of Computer Science and Engineering, JIS College of Engineering, Kalyani, Nadia, West Bengal, India, on the 4 th of July 2020.
24	Participated in the Webinar on "Ensuring Quality Assurance In Higher Education – Role Of Industry & Professional Bodies" organized by Internal Quality Assurance Cell (IQAC) and Department of Computer Science & Engineering under AICTE MARGDARSHAN on 1st June, 2020.
25	Attended an International Webinar on Block Level Design Using IP Integrator in Xilinx Vivado held on 26 th of June 2020 in association with CoreEL Technologies and Xilinx.
26	Attended the Webinar series "The vision and Goals with core values of IEEE, Technology Entrepreneurship Development, Role of IEEE 802 standards family during COVID-19 and post COVID-19 era," organised by IEEE student branch, Department of Electronics and Communication Acropolis Institute of Technology and research, Indore during 18-05-2020 to 20-05-2020.
27	Attended a One Week INUP hands-on training workshop on "Nano Fabrication Technologies" Held at IIT Bombay, Mumbai during October 22 nd to 26 th 2018.

28	Attended a One Day National Convention on “Research Trends in Wireless Communication Technologies” organized by Dept.of ECE, PVPSIT, in association with National Instruments India on 29 th July 2017.
29	Attended a Five Day Workshop on “VLSI Design” organized by APSSDC (Andhra Pradesh State Skill Development Corporation) Department of Skill development, Entrepreneurship & Innovation, Government of Andhra Pradesh during 15 th to 19 th Nov 2016 at Dept. of ECE, SRK Institute of Technology, Enekepadu, Vijayawada.
30	Attended a Five day Workshop on “Recent Advancements in VLSI Technology and Design Using EDA Tools” organized by Dept.of ECE, UCEK(A), JNTUK, KAKINADA during 20 th to 24 th July 2016.
31	Attended a Three day Workshop on “Custom IC Design and ASIC Implementation Using Cadence EDA Tools” organized by Dhanekula Institute of Engineering &Technology, Ganguru on 3 rd , 4 th and 5 th of August 2016.
32	Participated One Week Faculty Development Program (FDP) on “Outcome Based Education (OBE)” jointly organized by Electronics &ICT Academy, NIT Waranghal & Dhanekula Institute of Engineering and Technology, Vijayawada, during 06-06-2016 to 11-06-2016. And I Was awarded A+ (A-Plus) Grade in the Test conducted.
33	Attended Two day Faculty Development Program (FDP) on “Analog system Design using ASLK Pro” Organized by Dhanekula Institute of Engineering and Technology, Ganguru during 16 th to 17 th February 2016.
34	Attended a Two day Workshop on “Signal and Image processing Applications on Xilinx FPGAs” organized by Dept.of ECE,VR Siddhartha Engineering College, Vijayawada during 26 th to 27 th February, 2015.
35	Attended a Two day Workshop on “Advanced in VLSI Technology(NW-AVLSI 2013)” organized by Dept.of ECE,UCEK(A),JNTUK,KAKINADA during 26 th to 27 th April, 2013.
36	Attended One day Faculty Development Program (FDP) on “LATEX” Organized by Dhanekula Institute of Engineering and Technology, Ganguru on 4 th may, 2013.
37	Attended a Three day Workshop on “DSP Processors” organized by Dept.of ECE, mallareddy College of Engineering and Technology,Hakimpet,Secunderabad during 28 th to 30 th August 2008.
38	Attended a Three day Workshop on “Digital Design And Verification Using Verilog” organized by Dhanekula Institute of Engineering&Technology, Ganguru during 12 th to 14 th February 2013.
39	Attended a One day Faculty Development Program (FDP) on “JNTUK Centre of Excellence for e-resources Development & Deployment(CoEeRD)” organized by JNTUK CoEeRD and Globarena technologies Pvt.Ltd on 28 th August 2014.
40	Attended Three day Faculty Development Program (FDP) webinar on “ Networking Simulation using NetSim Software” Organized by DELLSOFT Technologies Pvt.Ltd during 15 th to 17 th June, 2016.
41	Attended Three One Faculty Development Program (FDP) webinar on “Circuit Simulation and PCB Design” Organized by DELLSOFT Technologies Pvt.Ltd during 14 th June, 2016.

List of Certification Courses Done		
NPTEL/SWAYAM		
1	NPTEL Online Certification: Digital Circuits (77%), Got ELITE Certification, Jul-Oct 2018, IIT Kharagpur. (12-week Course. No of Credits recommended by NPTEL:3)	
2	NPTEL Online Certification: Switching Circuits and Logic Design (84%), Got ELITE Certification, Jul-Oct 2019, IIT Kharagpur. (12-week Course. No of Credits recommended by NPTEL:3)	
Coursera Certifications		
	Name of the Course	Percentage Achieved
3	VLSI CAD-I: Logic	85

4	VLSI CAD-II: Layout	83
5	Developing FPGA accelerated cloud applications with SDAccel: Theory	98
6	Developing FPGA accelerated cloud applications with SDAccel: Practice	100
7	GPGA Computing systems: Background knowledge and introductory material	99
8	Introduction to OpenCL on FPGAs	100
9	Sensors and Sensors Circuit Design	91
10	Wireless communication for everybody	100
11	Introduction to programming with MATLAB	99
12	Introduction to communication science	100
13	Electric power systems	98
14	AI for everyone	100
15	Programming for everybody (getting started with Python)	98
16	Introduction to HTML	100

Achievements / Awards etc.,	
1.	UGC NET Qualified in ELECTRONIC SCIENCE: December 2014
2.	UGC NET Qualified in ELECTRONIC SCIENCE: June 2014
3.	GATE Qualified in EC: 2012
4.	GATE Qualified in EC: 2009



(Chandra Sekhar Savalam)