



DHANEKULAINSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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FACULTY PROFILE

Name of the Faculty NARAGAMSAIPRIYANKA
Designation Assistant Professor
Date of Joining 13-04-2023

Nature of Association Regular
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[8500473438](tel:8500473438)

Department Electronics and Communication Engineering



Educational Background

1. M.Tech from Teegala Krishna Reddy Engineering College, Hyderabad
2. B.Tech (ECE) from Annamacharya Institute of Engineering and Technology, Hyderabad

Areas of Specialization-

VLSI Design Total Experience - 4 years 3 months

Teaching/Research Months Experience. 3 years 6 months

Sl. No	Institute	Designation	Period
1	DJR college of engineering	Asst.Prof	November 2019 - Feb 2021
2	Usharama college of engineering and technology	Asst.Prof	Feb 2021 - April 2023
3	DhaneKula Institute of Engineering and Technology	Asst.Prof	April 2023 - till date

List of Publications (International Journals)-

Published the research paper titled "Implementation of A Interface Bridge for Axito OCPP Protocol" in International journal of science, engineering and technology research (IJSETR) ISSN 2319-8885 VOL.05, ISSUE-24 SEPTEMBER 2016

Achievements/Award setc.,

1. Received a Gold Medal for the academic excellence during M.Tech (2014-2016) at Teegala Krishna Reddy Engineering College.
2. Awarded with Microchip Educator Training Certificate on Microchip ATMEGA16
3. Ratified as Assistant professor by JNTUK.
4. Got Ph.D admission in JNTUK in January 2023.
5. Received a certificate for NPTEL COURSE on Python for Data Science.
6. Acting as a coordinator for Art, Literacy and Cultural Committee for ECE Dept.

Faculty Development Programs/Workshops:

1. Attended One week online Faculty Development Program AICTE sponsored on "VLSI Design using Symica EDA tool", organized by DhaneKula Institute of Engineering & Technology, held during 17.04.2023 to 19.04.2023.
2. Participated in the AICTE Recognized Faculty Development Program on Antenna Design Techniques and Tools Conducted by Electronics and Communication Engineering Department from 11/09/2023 to 15/09/2023 (One Week).
3. Attended One week online Faculty Development Program on "Enhancing expertise: Communication and Signal/Image Processing", organized by Dept of ECE, Aditya college of Engineering and Technology, held during 10.07.2023 to 15.07.2023.
4. Participated in the AICTE Recognized Faculty Development Program on "Open Source Tools for Research" Conducted by Computer Science and Engineering Department from 15/05/2023 to 19/05/2023 (One Week).
5. Attended 3 day National level Faculty Development Program on "Emerging Trends on VLSI Device, Circuit and System Design", organized by Dept of ECE, RVR & JC college of Engineering, held during 09.11.2023 to 11.11.2023.
6. Participated in the AICTE organized Faculty Development Program on "Inculcating Universal Human Values in Technical Education" from 22/05/2023 to 26/05/2023.
7. Attended one week Workshop on "VLSI to System Design: Silicon to end Application Approach", organized by AICTE, ARM Education and STMicroelectronics from 31.07.2023 to 04.08.2023.
8. Participated in the AICTE Training And Learning (ATAL) Academy Faculty Development Program on "Recent Advancements in VLSI Design and Fabrication": From Theory to Practice at DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY from 04/12/2023 to 09/12/2023.