



# DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE Affiliated to JNTU Kakinada)

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## FACULTY PRPOFILE

**Name of the Faculty** KRISHNA SALADI

**Designation** Assistant Professor

**Date of Joining** 26-10-2020

**Nature of Association** Regular



**Department**

Electronics and Communication Engineering

**Educational Background**

1. Ph.D (Pursuing ), JNTU Kakinada
2. M.Tech (VLSI & Embedded Systems) from RVCE, Bangalore
3. B.Tech (ECE) from AIET, Vizag

**Areas of Specialization**

VLSI Design

**Research Areas**

VLSI Signal processing

**Experience**

10 Years

52385

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No**

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Sl. No	Institute	Designation	Period
1	Dhanekula Institute of Engineering & Technology, Ganguru	Assistant Professor	26-10-2020 - till date
2	Aditya Engineering College, Surampalem	Assistant Professor	01-06-2016- 23-10-2020
3	Vignan University	Assistant Professor	02-07-2012 - 19-07-2014
4	Lovely Professional University	Assistant professor	21-07-2011-27-04-2012
5	NITW	Adhoc Faculty	13-07-2010-15-12-2011
6	APS College of Engineering	Lecturer	18-08-2009-14-06-2010

#### List of Publications (National & International Journals):

- 1.Sambaiah, P., Venkatareddy, V., Krishna, S., &Polaiah, G. (2014). A Tuned Resistive Feedback CMOS Low Noise Amplifier for Multiband Applications. International Journal of Applied Engineering Research, 9(19), 6413-6421.

#### List of Publications (National & International Conferences):

1. N. V. Satyanarayana, Krishna Saladi, R. Bharath Kumar “Review on Compressors based Approximate Multipliers Design”5th International Conference on Computing Methodologies and Communication (ICCMC 2021),08-10, April 2021
2. Krishna saladi and Leela Kumari B, “Adiabatic Logic-Based Area and Energy-Efficient Full Adder Design” International conference on Intelligent Computing in Control and Communication (ICCC 2020), August 7-8, 2020
3. Yamini Devi Ykuntam , Katta Pavani and Krishna Saladi ,”Design and analysis of High speed wallace tree multiplier using parallel prefix adders for VLSI circuit designs” Eleventh International Conference on Computing, Communication and Networking Technologies (ICCNT 2020),July 1-3
4. Venkatareddyvemireddy, Sambaiahpelluri and krishna saladi, “Low Power Reactive Feedback CMOS Low Noise Amplifier”, 2014 Communication and Signal Processing Conference (ICCSP), Chennai.
5. Krishna saladi and B S Kariyapaa “Design and Implementation of Compression Algorithms Using MIL-1553B bus” at NACTECIT-2009

#### Publication through students

1. Sasi Supritha Devi, Y., et al. "Analysis of precision agriculture technique by using machine learning and IoT." *Soft computing: theories and applications*. Springer, Singapore, 2020. 859-867.
2. Prasad, Bocha Dileep Venkata, et al. "Review on Different Types of Multipliers and Its Performance Comparisons." *International Conference on Emerging Trends and Advances in Electrical Engineering and Renewable Energy*. Springer, Singapore, 2020.

## Selected Workshops and FDP's

1. *AICTE Training And Learning (ATAL) Academy Faculty Development Program on **Recent Advancements in VLSI Design and Fabrication: From Theory to Practice** at **DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY** from **04/12/2023** to **09/12/2023**.*
2. *Workshop on **VLSI to System Design : Silicon to End Application Approach** Organized by AICTE ,ARM edutech and ST microelectronics from July 31<sup>st</sup> to Aug 04*
3. Pedagogy workshop on **Buliding an online signals and systems course part-I** conducted from 27 to 30 November 2020 by TEQIP-III KITE, PPCCLT (IIT Bombay) and Teaching Learning Centre (ICT) under PMMMMNMTT, MoE, Govt. of India.
4. AICTE sponsored one week online short-term program on **INNOVATIVE TEACHING LEARING METHODS FOR INSPIRING STUDENTS (ITLMIS 2020)**organized by Dept of ECE Sagi Ramakrishna Raju Engineering college (A) Bhimavaram during 18th to 23rd December 2020.
5. Five-day workshop on **inspiring Engineers through innovation, creative models and machines** Conducted at IIT Gnadhinagar from 12<sup>th</sup> march to 16<sup>th</sup> march ,2019
6. Five-day faculty development program on **VLSI DESIGN** conducted by APSSDC & LUCID VLSI technologies at SRK Institute of Technology
7. One-week National workshop on **Recent Advancements in VLSI Technology and Design Using EDA Tools (VLSITP-2016)** organized by JNTU Kakinada.
8. Five Day National workshop on **Recent Advancements in VLSI Technology and Design Using EDA Tools (VLSITP-2016)** Conducted by CoreEL Technologies Pvt. Ltd
9. One Week **Faculty Induction Programme** conducted by experts and both industry & Academia at Vignan University.
10. Two-week ISTE Workshop on **Analog Electronics** conducted by IIT Kharagpur at National Institute of Technology, Warangal.
11. Five-day Customized Training Program on **ARM Cortex M3 Microcontroller** at NIELIT, Calicut.
12. Three Day workshop on **Embedded Processors** Conducted by JNTUH, Hyderabad.

## Achievements / Awards etc.,

1. **UGC NET Qualified** : **April 2014**
2. **GATE Qualified** :**2013**
3. **GATE Qualified** :**2007**



