DHANEKULA INSTITUTE OF ENGINEERING AND TECHNOLOGY
(Autonomous)

Department
Electronics and Communication Engineering

VLSI VISIONARIES CLUB

OVERVIEW

The VLSI Club is a collaborative space for students to learn the fundamentals of Very Large-Scale Integration design. It focuses on hands-on projects guided by experienced faculty members, ensuring a deep understanding of core concepts. Regular workshops cover essential topics, tools, and techniques in VLSI design. Students work in teams to tackle practical projects, fostering teamwork and creativity. The club promotes a supportive environment where members can seek guidance and feedback from faculty. Additionally, it encourages experimentation and innovation in project development. Students gain valuable experience that enhances their academic learning. Networking opportunities with peers and faculty help build a strong community. Overall, the club is an ideal platform for students to develop their skills and apply their knowledge in real-world scenarios.

- Club Name: VLSI Visionaries
- Moto: Design Smart, Build!
- Club Leadership: Mr.S. Chandra Sekhar
- President:Mr. Chandu Ram- 4th year
- Vice-President:Ms. Harshitha – 3rd year

Executive members:

1. Ms. Saranya – 4th year
2. Ms. Jyothirmai – 4th year
3. Mr. Ram Reddy – 4th year
4. Ms. Yuktha Mukhi- 3rd year
5. Ms. Himaja- 3rd year
6. Ms. Fathima– 4th year

Aim: To create awareness about the latest developments in the core VLSI domain and to bring out the technical innovative talents of the students.

Values:

- **Collaboration**: Teaming up to achieve shared goals in VLSI design.
- **Innovation**: Encouraging creative thinking and new approaches to challenges.
- **Integrity**: Committing to honesty and transparency in all our endeavours.
- **Learning**: Promoting continuous growth and knowledge sharing among members.
- **Support**: Creating a welcoming environment that values every member's contribution

Vision of the Club: *To keep the students abreast of the recent developments in the VLSI domain area and provide space to exhibit their technical skills.*

Mission of the Club*: Inspire students to update technical skills and showcase them through technical displays in the field of VLSI.*

Activities Planned:

- Awareness Sessions on VLSI simulation software
- Seminar / Workshops
- Technical Presentations
- Project Display
- Certifications
- Internships

GOALS:

- ***Educating Students about the VLSI.***
- ***Participating in Hackathons, Competitions and Tech events***
- ***Promote Professional Exploration And Networking***


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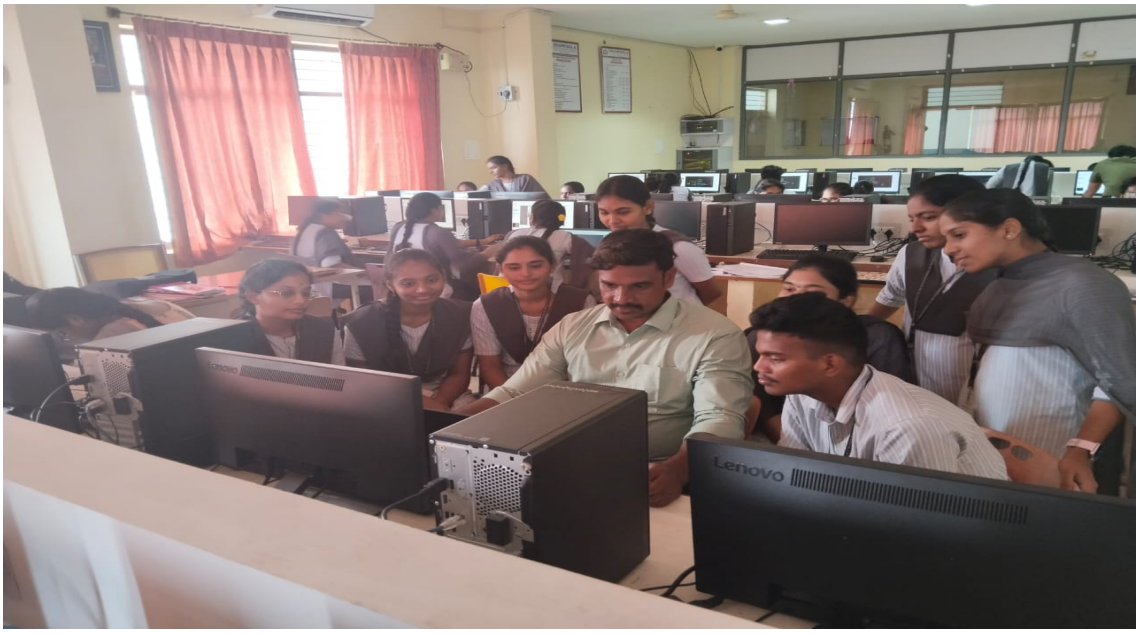
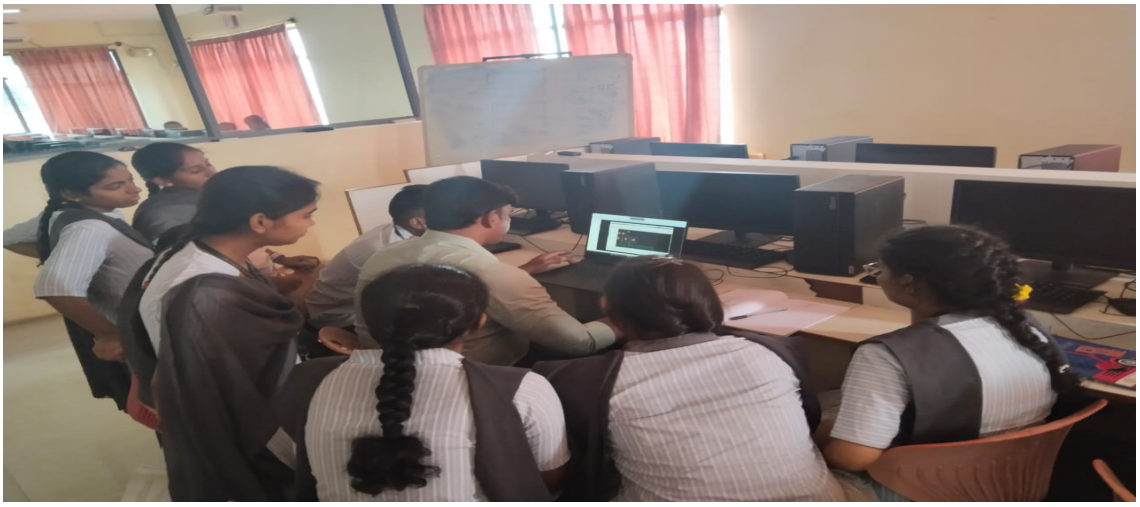
Goal 1: Educating Students about the VLSI.



Empowering the Next Generation of Innovators!

At our VLSI Club, we believe that education is the foundation for innovation. Our first goal is to provide comprehensive knowledge about VLSI, its technology, and its applications in the modern electronics industry.This emphasizes both the educational mission and the focus on VLSI technology.



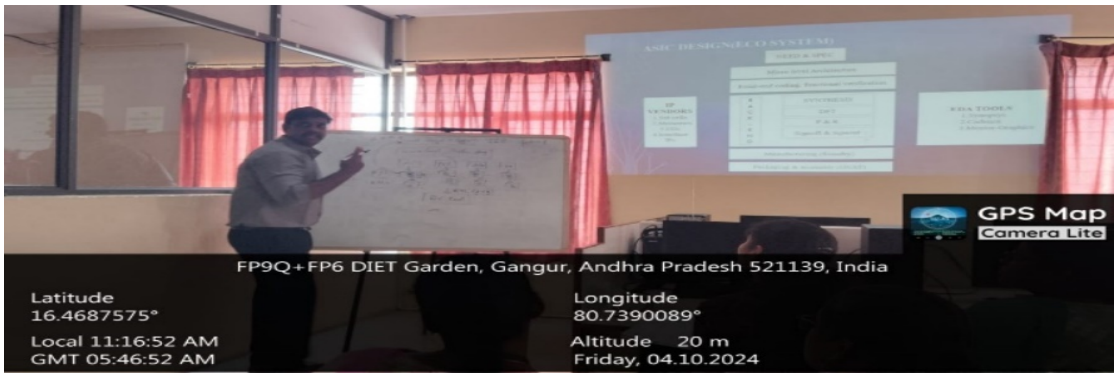
Goal 2: Participating in COMPETETIONS AND TECHEVENTS.

- ***Hands-On Experience:****Applywhat you've learned I nreal-world scenarios.*
- ***Teamwork:*** *Collaborate with fellow club members andfoster teamwork.*
- ***Feedback:*** *Gain insights from judges and industryexperts to improve your projects.*
- ***Recognition****: Showcase your innovative ideas and solutions to a broader audience.*

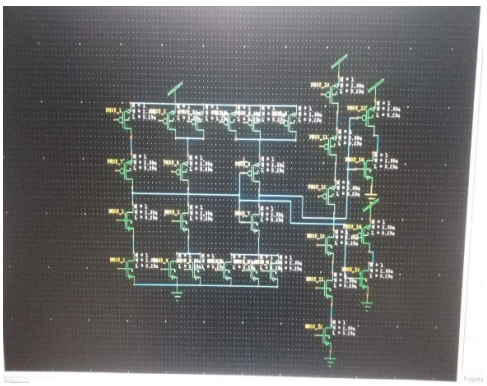
Goal3: Promote PROFESSIONAL EXPLORATION AND NETWORKING

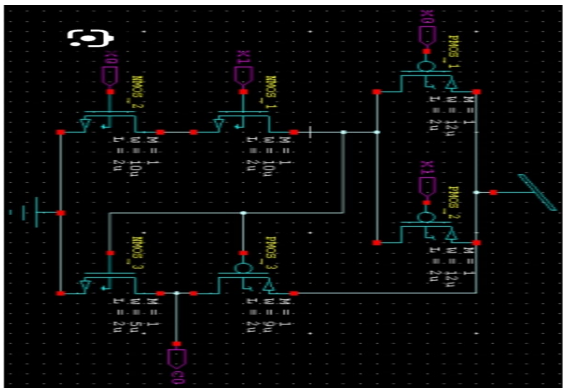
Facilitate opportunities for students to connect with industry professionals, alumni, and experts in the VLSI field.

VLSI club learning process

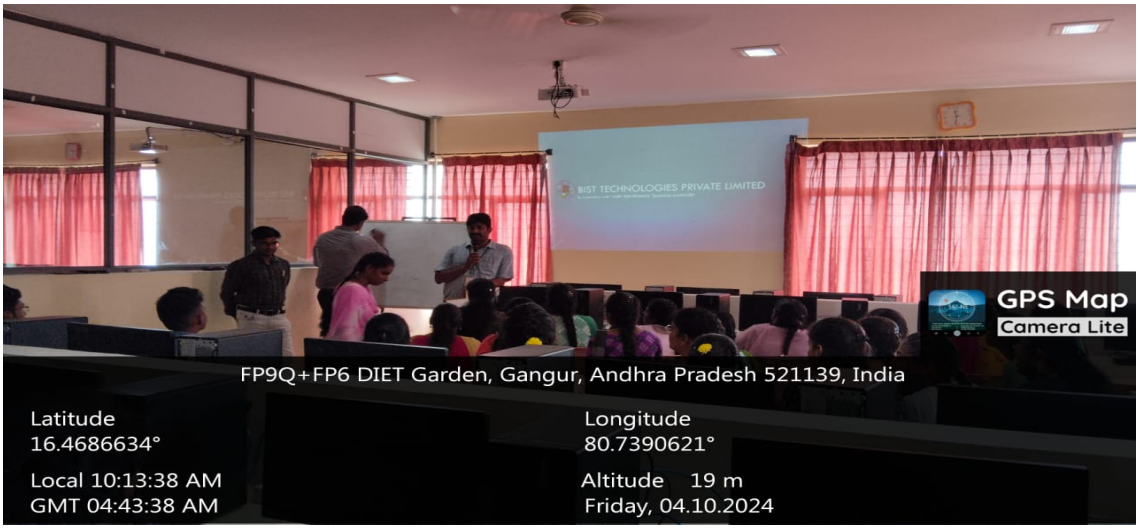


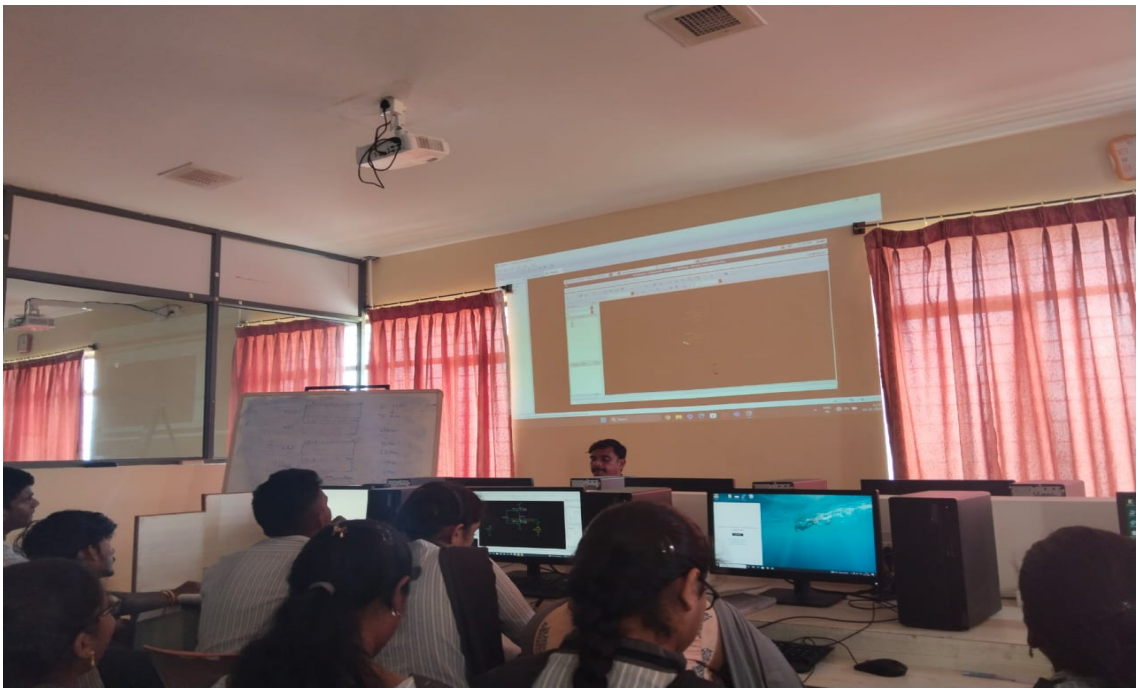
Collaborative Projects





Innovation Days





FACULTY INVOLVED:

Mr. S.Chandra Sekhar

Mr. Krishna Saladi

Mrs. Mahalakshmi

Mrs. B.Durga Sailaja

STUDENTS INVOLVED:

Ch. Himaja	S. Maha Lakshmi
Ch. Yutha mukhi	Harika
D. Anada Saraswathi	S. Jithendra
B. Siva	D. Bharat Sai
A. Sairam	G. Lakshmi Priyanka
A. Sivasri	M. Hema
Ch. Mounika	K. Ankitha
M. Akhila	P. Lavanya
M. Vani	M. Vani
k. Uday Kiran	J. Sukanya
A. Anand	B.Nikhila
U. prem Kumar	K. Yashwanth
Y.Yasin	Hameeda
M. Sudheer	Ruksar
T. Gayathri	Jyothirmai
T. Harika	Fathima
P. Likitha	Mythili
S. Uma Jayasree	Naga Jyothi
P. Raja Lakshmi	Rajini
T. Teja	G. Dhanesh
K. Uday Kiran	Vinod kumar
U. Prem Kumar	Rami Reddy
P. Likitha	Tulasi Ram
Rajalakshmi	A. Sravanthi
U.Keerthi	S. Praneeth
Sk.Gulshan	k. Pavani
M.Naga Abhishek	B.Siva

ACTIVITIES AND EVENTS

Monthly Meetings:

- ***Foster Collaboration***
  Encourage teamwork and idea-sharing on VLSI projects and initiatives.
- ***Community Building***
  Strengthen bonds among members and create a supportive learning environment.
- ***Project Planning***
  Outline and strategize ongoing VLSI projects, ensuring progress and goal alignment.
- ***Feedback and Improvement***
  Gather feedback on activities and projects, continually enhancing the club experience.

Activities:

- ***Competitions and Challenges***
  Engage members in VLSI-focused competitions to test and showcase their skills.
- ***Team-Building Activities***
  Organize sessions to enhance collaboration, trust, and group dynamics.
- ***Hackathons***
  Participate in or host hackathons focused on VLSI, encouraging creative problem-solving.
- ***Feedback and Reflection Sessions***
  Regularly review activities to gather insights, celebrate successes, and discuss improvements.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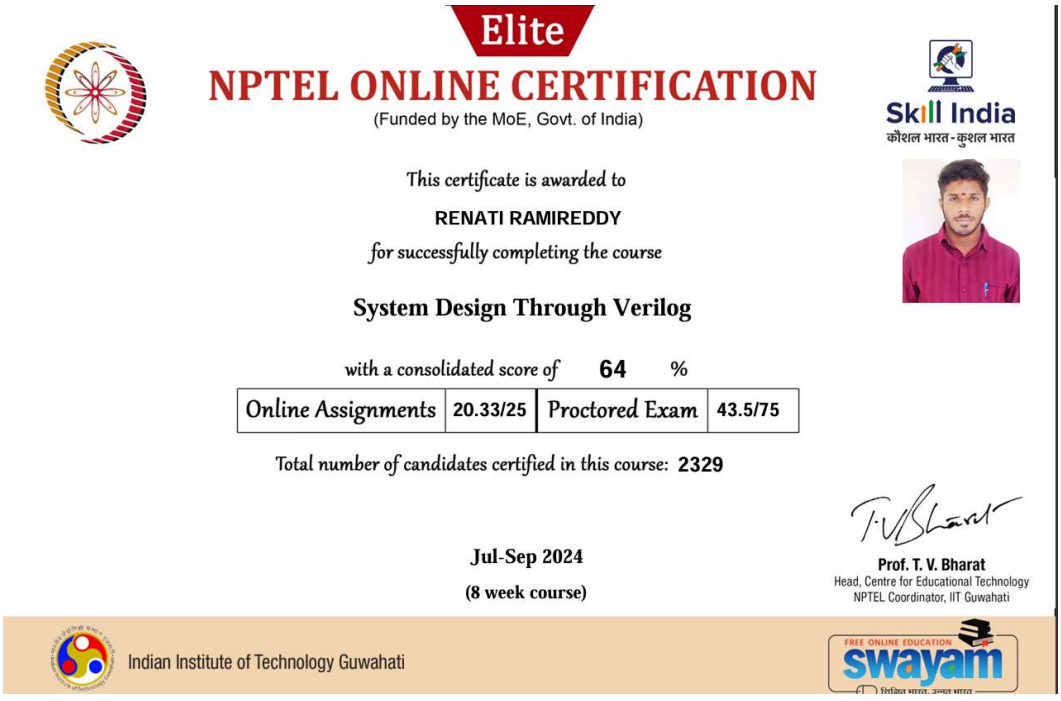
Workshops and Training sessions

- Boot Camp on VLSI Basics & Design Fundamentals
  Offer an intensive introductory boot camp covering VLSI concept, circuit design, and layout fundamentals.
- Value added courses on VLSI Software Tools
  Conduct master classes on industry-standard VLSI tools like Cadence, Synopsys, and Mentor Graphics.
- Internships Provided by BIST AND BLACKBUCKS
  Partner with industry leaders to offer internship opportunities, giving students hands-on experience in VLSI.

Certifications:



Elite

NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)

Skill India
कौशल भारत-कुशल भारत

This certificate is awarded to

RENATI RAMIREDDY

for successfully completing the course

System Design Through Verilog

with a consolidated score of 64 %

Online Assignments	20.33/25	Proctored Exam	43.5/75

Total number of candidates certified in this course: 2329

Jul-Sep 2024

(8 week course)

Prof. T. V. Bharat
Head, Centre for Educational Technology
NPTEL Coordinator, IIT Guwahati

Indian Institute of Technology Guwahati

FREE ONLINE EDUCATION swayam

NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)

Skill India
कौशल भारत-कुशल भारत

This certificate is awarded to

KONANKI VINOD KUMAR

for successfully completing the course

System Design Through Verilog

with a consolidated score of **50** %

Online Assignments	20.33/25	Proctored Exam	30/75

Total number of candidates certified in this course: **2329**

Jul-Sep 2024

(8 week course)

Prof. T. V. Bharat
Head, Centre for Educational Technology
NPTEL Coordinator, IIT Guwahati

Indian Institute of Technology Guwahati

FREE ONLINE EDUCATION swayam

Roll No: NPTEL24EE94S449601639 To verify the certificate No. of credits recommended: 2 or 3

Elite

NPTEL ONLINE CERTIFICATION

(Funded by the MoE, Govt. of India)

Skill India
कौशल भारत-कुशल भारत

This certificate is awarded to

KORLAGUNTA TULASI RAM

for successfully completing the course

System Design Through Verilog

with a consolidated score of **71** %

Online Assignments	20.33/25	Proctored Exam	51/75

Total number of candidates certified in this course: **2329**

Jul-Sep 2024

(8 week course)

Prof. T. V. Bharat
Head, Centre for Educational Technology
NPTEL Coordinator, IIT Guwahati

Indian Institute of Technology Guwahati

FREE ONLINE EDUCATION swayam

ANDHRA PRADESH STATE COUNCIL OF HIGHER EDUCATION

(A Statutory Body of the Government of A.P.)

Certificate of Completion

Certificate Id: **BBAPSCHEIIDT2024ST102035**

This is to certify that **HAMEEDA AFREEN SHAIK** , bearing Reg. No: **218T1A04B3**, from **Dhanekula Institute of Engineering and Technology** of **JNTU Kakinada**, has successfully completed a **Short-term internship** for **8 Weeks** on **VLSI.** This internship was organized by **International Institute of Digital Technologies**, with its **industry partner Blackbuck Engineers**, in association with the **Andhra Pradesh State Council of Higher Education (APSCHE).**

Anuradha Thota
Chief Executive Officer
Blackbuck Engineers Pvt. Ltd.

Dr. Sundar Balakrishna
Director General
International Institute of Digital Technologies

Date: 24/07/2024 **Place**: Tirupati, Andhra Pradesh

ANDHRA PRADESH STATE COUNCIL OF HIGHER EDUCATION

(A Statutory Body of the Government of A.P.)

Certificate of Completion

Certificate Id: **BBAPSCHEIIDT2024ST102227**

This is to certify that **K. JYOTHIRMAI BHARGAVI**, bearing Reg. No: **218T1A04B8**, from **Dhanekula Institute of Engineering and Technology** of **JNTU Kakinada**, has successfully completed a **Short-term internship** for **8 Weeks** on **VLSI.** This internship was organized by **International Institute of Digital Technologies**, with its **industry partner Blackbuck Engineers**, in association with the **Andhra Pradesh State Council of Higher Education (APSCHE).**

Anuradha Thota
Chief Executive Officer
Blackbuck Engineers Pvt. Ltd.

Dr. Sundar Balakrishna
Director General
International Institute of Digital Technologies

Date: 24/07/2024 **Place**: Tirupati, Andhra Pradesh

VLSI Student Club serves as an inspiring platform for students to delve into the fascinating world of microelectronics and chip design. Through hands-on projects, competitions, and collaborative learning, members not only build essential technical skills but also develop teamwork and problem-solving abilities. With a focus on real-world applications such as semiconductor design and embedded systems, the club prepares members for impactful careers in electronics and technology. As we continue to explore and innovate, the club remains dedicated to fostering a community of passionate learners ready to shape the future of VLSI and semiconductor technology.