

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY
Department of Electronics & Communication Engineering



DIET/7.5.1/FT 12A

UG TIME TABLE

Name of the Program: B. Tech in ECE

Class/Sem: III B. Tech SEM-II

Sec: A

Class Room No: F-40

A. Y: 2024-25

w.e. f: 10.12.2024

DAY	1 9:00-9:50	2 9:50-10:40	3 10:50-11:30	4 11:30-12:20	5 12:20-1:10		6 2:00-2:50	7 2:50-3:40	8 3:40-4:30
MON	CN	DSP	MPMC/ARM Skill Lab			L U N C H	CMC (T)	VLSI	RM
TUE	CMC	VLSI	Association				DSP	MPMC	Library
WED	VLSI	CMC	CN	DSP (T)	MPMC		DSP/VLSI Lab		
THU	DSP	MPMC	CMC	CN	VLSI (T)		ITC		
FRI	CN	DSP	MPMC (T)	VLSI	CMC		VLSI/DSP Lab		
SAT	MPMC	CN (T)	ARM Skill /MPMC Lab				RM	Counseling	Sports

Note: T – Tutorial Hour.

Theory/Lab	Faculty Name	L	T	P	O	Sign
Microprocessors and Microcontrollers	Mrs.Y.Naga Prasanthi	4	1	--	--	Y.Naga
Digital Signal Processing	Mr. R. Chandra Sekhar	4	1	--	--	R
VLSI Design	Mr. S. Chandra Sekhar	4	1	--	--	R
Computer Networks (OE2)	Mrs. G Mary Silvy	4	1	--	--	G
Mobile and Cellular Communications (PE2)	Mrs. B Prathyusha	4	1	--	--	B
Research Methodology	Mr. N SivaNagaraju	2	--	--	--	N
Microprocessors and Microcontrollers Lab	Ms.Y.Naga Prasanthi	--	--	3	--	Y.Naga
Digital Signal Processing Lab (F-42 B)	Mr. R. Chandra Sekhar	--	--	3	--	R
VLSI Design Lab (F-42 A)	Mr. S. Chandra Sekhar	--	--	3	--	S
Arduino/ARM Skill Lab (IoT Lab)	Mr. N. Naga Raju	--	--	3	--	N
ITC(EEE Simulation lab)	Mrs. G Durga Bhavani	--	--	3	--	G
Association()	Mr. Ch Ramesh Babu	--	--	--	3	Ch
Sports	Mr.S.Chandrasekhar	--	--	--	1	S
Library	Mr. R. Chandra Sekhar	--	--	--	1	R
Counselling	Respective Counsellors	--	--	--	1	
		48				
Class In charge	Mrs.Y.Naga Prasanthi					Y.Naga

(Signature)
I/C Time Tables/Date
06/12/2024

(Signature)
HOD Date 6/12/24
HEAD OF THE DEPARTMENT
Electronics & Communication Engineering
DHANEKULA INSTITUTE
OF ENGINEERING AND TECHNOLOGY
GANGURU, VIJAYAWADA

(Signature)
Principal

