

DHANEKULA INSTITUTE OF ENGINEERING & TECHNOLOGY
Department of Electronics & Communication Engineering



DIET/7.5.1/FT 12A

UG TIME TABLE

Name of the Program: B. Tech in ECE

Class/Sem: III B. Tech SEM-II

Sec: B

Class Room No: F-41

A. Y: 2024-25

w.e. f: 10.12.2024

DAY	1 9:00-9:50	2 9:50-10:40	3 10:50-11:30	4 11:30-12:20	5 12:20-1:10	6 2:00-2:50	7 2:50-3:40	8 3:40-4:30
MON	VLSI	CMC	VLSI/DSP Lab			DSP	MPMC	Library
TUE	MPMC	CN	CMC	DSP	VLSI (T)	MPMC/ARM Skill Lab		
WED	CN	DSP	MPMC (T)	VLSI	CMC	ITC		
THU	RM	CN	VLSI	MPMC	DSP (T)	MPMC / ARM Skill Lab		
FRI	CMC	VLSI	DSP / VLSI Lab			MPMC	CN (T)	RM
SAT	DSP	CMC (T)	Association			CN	Counseling	Sports

Note: T - Tutorial Hour.

Theory/Lab	Faculty Name	L	T	P	O	Sign
Microprocessors and Microcontrollers	Mrs. Y. Naga Prasanthi	4	1	--	--	Y. Naga Prasanthi
Digital Signal Processing	Mr. R. Chandra Sekhar	4	1	--	--	R. Chandra Sekhar
VLSI Design	Mr. S. Chandra Sekhar	4	1	--	--	S. Chandra Sekhar
Computer Networks (OE2)	Mrs. G Mary Silvy	4	1	--	--	G. Mary Silvy
Mobile and Cellular Communications (PE2)	Mrs. B Prathyusha	4	1	--	--	B. Prathyusha
Research Methodology	Mr. N SivaNagaraju	2	--	--	--	N. SivaNagaraju
Microprocessors and Microcontrollers Lab	Mrs. B Prathyusha	--	--	3	--	B. Prathyusha
Digital Signal Processing Lab (F-42 B)	Mrs. G Mary Silvy	--	--	3	--	G. Mary Silvy
VLSI Design Lab (F-42 A)	Mr. Ch Ramesh Babu	--	--	3	--	Ch. Ramesh Babu
Arduino/ARM Skill Lab (IoT Lab)	Mr. Md. Abdul Aziz	--	--	3	--	Md. Abdul Aziz
ITC(EBE Simulation lab)	Mr. Ch Ramesh Babu	--	--	3	--	Ch. Ramesh Babu
Association()	Mrs. G Durga Bhavani	--	--	--	3	G. Durga Bhavani
Sports	Mrs. Y. Naga Prasanthi	--	--	--	1	Y. Naga Prasanthi
Library	Mrs. G Mary Silvy	--	--	--	1	G. Mary Silvy
Counselling	Respective Counsellors	--	--	--	1	
		48				
Class In charge	Mr. S. Chandra Sekhar					

(Signature)
 I/C Time Tables/Date
 26/12/2024

(Signature)
 HOD/Date
 26/12/24
 DEPARTMENT
 Electronics & Communication Engineering
 DHANEKULA INSTITUTE
 OF ENGINEERING AND TECHNOLOGY
 Bangalore, VIJAYAWADA-521 15

(Signature)
 Principal

