



DHANEKULAINSTITUTE OFENGINEERING&TECHNOLOGY

(Autonomous)

GANGURU::VIJAYAWADA-521139.

(Approved by AICTE New Delhi, Permanently Affiliated to JNTU
Kakinada) ISO9001:2015 Certified Institution, Accredited by NBA for ME, EEE, EC
E&CSE.

E-mail: diet.principal@gmail.com, principal@diet.ac.in, website: www.diet.ac.in, Phone: +91-8333924842, 8333924843

FACULTY PROFILE

Name of the Faculty: Dr. J. Ankamma Rao

Designation: Associate Professor

Email & Phone No: jaraoeee04@gmail.com & 7995954146

Department: Electrical and Electronics Engineering.



Educational Background:

1. B.E. From Gandhi Institute of Technology and Management (GITAM), Visakhapatnam, A.P
2. M.Tech From Visveswaraya National Institute of Technology (VNIT), Nagpur.
3. Ph.D from Sri Satya Sai University of Technology & Medical Sciences, Sheore.

Research Areas: Applications of AI in Power Systems and Power System Protection

Experience : 13 Years

S.No	Institute	Designation	Period
1	DIET, Ganguru, Vijayawada, A.P	Associate Professor	March 2025 to till date
2	Assosa University, Ethiopia	Associate Professor	October 2023 to Nov. 2024
3	Vignan Institute of Information Technology, Visakhapatnam, A.P.	Associate Professor	January 2023 to Sept. 2023
4	Samara University, Ethiopia	Assistant Professor	Oct. 2013 to July 2022
5	Sharda University, Greater Noida, U.P.	Assistant Professor	June 2012 to Sept. 2013
6	G. Narayanamma Institute of Technology & Sciences, Hyderabad.	Assistant Professor	July. 2008 to May 2010

List of Publications (International Journals)

1. N. Praneeth, S. Srinivas, A. M. Parimi, J. Ankamma Rao and G. T. Zelig, "Finding Optimal Zone II Settings for Distance Relays in Coordination with Directional Overcurrent Relays Using QCQP Algorithm," in IEEE Access, doi: 10.1109/ACCESS.2024.3466557.
2. L. N. S. Varanasi, A. R. Jonnalagadda and S. P. K. Karri, "Smart Edge Device Utilizing Power Line Communication for Energy Management and Control of Electrical Appliances," in IEEE Access, vol. 12, pp. 37207-37218, 2024, doi: 10.1109/ACCESS.2024.3373009.

3. M.D.Amentie, M.M.Tulu,A.R.Jonnalagadda, "Exploiting Collaborative Behavior for User QoE Enhancement in Device-to-Device Networks " inTuijin Jishu/Journal of Propulsion Technology, Vol. 45 No. 3 (2024):2576-2597
4. T Eswara Rao, S Srinivasa Rao, J Ankamma Rao, B Trinadha, P Guruvulu Naidu, Vakada Krishna, P Bharat Kumar, B Naidu,"Real time Implementation of PV-Wind Hybrid System using FPGA-MPPT Controller" in J.Electrical Systems,20-3(2024):2956-2968.
5. Ankamma Rao J, Dr.Saradar Ali "A Study on Dynamic Security Assessment and Early and Reliable Identification of The Critical Machine Cluster" published in RESEARCH REVIEW International Journal of Multidisciplinary [UGC Listed Journal; Journal No:44945].Volume.4, Issue.03,Page.2846-2850, March 2019, e-ISSN:2455-3085, Impact Factor:5.214
6. Ankamma Rao J, Dr.Saradar Ali "A Study on Fast Contingency Screening and On-line Transient Stability Assessment" published in RESEARCH REVIEW International Journal of Multidisciplinary [UGC Listed Journal; Journal No:44945].Volume.4, Issue.01,Page.2655-2659, January 2019, e-ISSN:2455-3085, Impact Factor:5.214.
7. Asefa Sisay, Ankamma Rao J "Power Factor Improvement in Distribution System by Using Shunt Capacitor Case Study on Samara University, Ethiopia",. International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE), Vol.6 Issue 08, August-2017, pp.6264-6270.
8. Ankamma Rao J, BizuayehuBogale, AsefaSisay "Standalone Solar Power Generation for Supply as Backup Power to Samara Univerity in Ethiopia",. International Journal of Engineering Research& Technology (IJERT), Vol.6 Issue 05, May-2017, pp.582-586.
9. Ankamma Rao J, BizuayehuBogale, AsefaSisay "Analysis of performance of Impedance Based and Travelling Based Fault Location Algorithms on 400Kv Transmission Line", International Journal of Engineering Research& Technology (IJERT),Vol.6 Issue 02, February-2017,pp.1-5.
10. Ankamma Rao J,BizuayehuBogale, Jemal Mohammed Amin, Tsegaye Nagesh " Fault Location Calculation Based on Two Terminal Data of High Voltage Transmission Line", Inter National Journal of Research in Engineering &Technology(IJRET),Vol.4, Issue 10,Oct2016,pp.1-6.
11. Ankamma Rao J, GebreegiabherHagoos,"Review of Performance of Impedance Based and Travelling Based Fault Location Algorithms in Double Circuit Transmission Lines",Journal of Electical and Electronic Engineering2015;3(4);pp.65-69.
12. Kiran Kumar K, Harish CH, Ankamma Rao J, "Reduction of Torsional Oscillations in large Turbine-Generator set using thyristor controlled braking resistors (TCBR)",International Journal of Electrical Engineering &Technology(IJEET),Vol.6,Issue.4, April-2015,pp.30-38.
13. Ankamma Rao J, GebreegiabherHagoos "Comparison of Accuracy of Various Impedance Based Fault Location Algorithms on Power Transmission Lines ",International Journal of Science and Research (IJSR), Vol.4, Issue.3, March-2015, pp.1869-1873.

14. Ankamma Rao J, Bizuayehu Bogale "Accurate Fault Location Technique on Power Transmission Lines with use of Phasor Measurements", International Journal of Engineering Research & Technology (IJERT), Vol. 4, Issue.02, February - 2015, pp.492-495.
15. Ankamma Rao J, BizuayehuBogale "Double Circuit Transmission Line Fault Distance Location using Wavelet Transform and WMM Technique ", International Journal of Science and Research (IJSR), Vol.4, Issue.1, January-2015, pp.32-36.
16. Ankamma Rao J, SrinivasS.T.P, Venkata Prasanth.B, "Fault Location Identification Using Wavelet Transform and WMM Technique", International Journal of Electronic and Electrical Engineering(IJEEE),Vol.7,Number1(2014),pp.59-69
17. AnkammaRaoJ,SrinivasS.T.P,Venkata Prasanth B, "Mitigation of Torsional Oscillations in Turbine Generator Set using Gate Controlled Series Capacitor",International Journal of Research in Engineering and Technology (IJRET),Vol.2, Issue.4, Apr 2014,pp.151-160.
18. Ankamma Rao J, S.T.P.Srinivas ,Venkata .B, "Damping of Power Oscillations in series compensated Transmission Line Using Gate Controlled Series Capacitor", International Journal of Science and Research (IJSR), Vol.3, Issue.3,March-2014,pp.578-583.

Patents:

1. Thamatapu.,Jonnalagadda.,Melkamu.,Getie.,Abera.,Botta.,Guruvulu.,P.Maheswara.,B.Diwakar,K.Avinash. (2024). **A system and method to analyze performance of super capacitor for energy storage.**
2. Ketullkumar., Jonnalagadda.(2024). **Machine Learning Based Decision Trees for Energy Meter Inspection in Power Sector.**

Text Books/Book Chapters:

1. G.Kavitha, P.Latchoumy, A.R.Jonnalagadda ,P.Ganasekaran (2024). **Fundamentals of Data Structures** in Scientific International Publishing House.ISSN NO:978-93-6674-080-5
2. Alpna Rani, Mandeep Singh Katre, Dr. Jonnalagadda Ankamma Rao, & Karishma Singh. (2025). **Layout Managers in Java Programming.** In Layout Managers in Java Programming. ChyrenPublication.ISSN NO:978-93-49076-45-7

Conferences/Workshops:

- Presented paper entitled '**Performance Analysis of Super Capacitor for Energy Storage**' in International Conference on Communications and Cyber-Physical Engineering, 19-20, July 2024 organized by G.Narayanamma Institute of Technology &Sciences , Hyderabad, India (Under Publication process in Springer)
- Presented a paper titled Solar and Wind based Wireless Power Transmission for Electric Vehicles at the 2024 IEEE Flagship International BIT Conference (BITCON-24), theme "Next

Generation Applications in Green Energy Technology”, organized by the Department of Electrical Engineering, BIT Sindri, Dhanbad (828123), Jharkhand, India and technically co-sponsored by IEEE Kolkata Section scheduled during 7th-8th December 2024.(Under Publication process in IEEE)

- Presented a paper titled JONNALAGADDA ANKAMMA RAO has Farming 4.0: Integrating Technology for Smart Agriculture at the 2024 IEEE Flagship International BIT Conference (BITCON-24), theme “Next Generation Applications in Green Energy Technology”, organized by the Department of Electrical Engineering, BIT Sindri, Dhanbad (828123), Jharkhand, India and technically co-sponsored by IEEE Kolkata Section scheduled during 7th-8th December 2024.(Under Publication process in IEEE)
- T. E. Rao, B. Naidu, J. Ankamma Rao, G. T. Zelig, K. Avinash and M. Srisuresh, "Soft Computing Based Hybrid MPPT for Grid Connected Photovoltaic System," 2024 International Conference on Electronics, Computing, Communication and Control Technology (ICECCC), Bengaluru, India, 2024, pp. 1-6, doi: 10.1109/ICECCC61767.2024.10593845
- Participated in the conference “The Role Natural and Computational Sciences in Supporting the Livelihood of Pastoral and Agro Pastoral Community” organized by the College of Natural and Computational Sciences, Samara University, Semera from 26-05-2017 to 27-05-2017.
- Participated online Lecture Intellectual Property Rights(IPR) in Social Sciences organized by IPR committee under Research & Development Cell(RDC),IQAC, Marathwada College of Education on 9th December 2024.
- Participated in the **4 days International Workshop on “ Artificial Intelligence Tools for Information and Knowledge Management ”** from 4th to 7th October 2024 ,organized by The Tamil Nadu Dr Ambedkar Law University.
- Participated in National Virtual Workshop on **online Research Tools & Techniques** organized by SSVPS’s B.N.S.Patil Arts and M.F.S.A. Commerce College, Deopur, Dhule, Maharashtra in association with ELTAI khandesh chapter on 19th September 2024.
- Participated in National Level Online Workshop on “**Career and Higher Education Opportunities for Graduates**” jointly organized by Insitute of Public Enterprise (IPE), Hyderabad, Govt. Degree College, Koratla&GirrajGov.College, Nizamabad on May 8,2024.
- Participated in 2 Days Workshop on “**Project Writing for Grants (How to Write a project & Apply for Grants) & Research Paper Writing**” organized by Research foundation of India & RFI-CARE from 11th March to 12th March 2024.
- Participated in the One Day Workshop on “ Digital Marketing Framework” organized by Research foundation of India & RFI-CARE on 10 February 2024.
- Conducted workshop on **Matlab software** in Samara University, Afar Region and Ethiopia from 13-03-2015 to 15-03-2015.
- Participated in the workshop “**Two Day Workshop on Applications of Soft Computing Techniques in Electrical Engineering** ”organized by Department of Electrical & Electronics

Engineering ,Vignana Bharathi Institute of Technology, Hyderabad from 23-04-2010 to 24-04-2010.

Achievements/Awards :

1. Received post graduate fellowship during M.Tech in 2010-2012 sponsored by MHRD,India.
2. Received Academic Excellence Prize for securing highest CGPA 9.89 in M.Tech.Integrated Power Systems Programme concluded in Spring 2012.
3. Received Certificate of Recognition for active role in research undertaking by the Samara University, Ethiopia and produced works that lead to publications in national and international journals that promote university for 2016-17.
4. Received certificate of Appreciation for excellent execution as member of team for establishment of Daagu International Journal of Basic and Applied Research(DIJBAR), Samara University,Ethiopia.
5. Received certificate of appreciation for active and valuable participation in Electrical & Computer Engineering department by motivating staff by preparing seminars and generating brain storm ideas beside duties during September 2014.
6. Received 50% discount in Intermediate education fee for securing 196 out of 200 in maths and psychics in 10th class.

Guest Lecture:

Guest Lecture given on ‘**Fault Location Identification on 400Kv Transmission Line Using WMM & Wavelet Transform**’ in Electrical Engineering Department , VIT, Vellore on 04 September,2024.

PhD Thesis Evaluation:

1. ENHANCING OF BREAST CANCER HISTOPATHOLOGY IMAGE CLASSIFICATION AND DETECTION OF MALIGNANCY THROUGH ADVANCED DEEP LEARNING TECHNIQUES” submitted by Mr. BHAVANNARAYANNA KOLLA of the School of Electronics Engineering, for the award of the degree of Ph.D. of VIT, Vellore.
2. EFFICIENT CHANNEL ESTIMATION IN MIMO COMMUNICATION SYSTEMS USING DEEP ENSEMBLE LEARNING” submitted by Ms. B.M.R MANASA of the School of Electronics Engineering, for the award of the degree of Ph.D. of VIT, Vellore.

Dr.Jonnalagadda Ankamma Rao