

Principal's Message



Dear Parents and Students,

It is with great pleasure that I welcome you to our College (DIET) Newsletter.

As Principal I am hugely impressed by the commitment of the college and the staff in providing an excellent all-round education for our students with our state of the art facilities. We as a team working together, strongly promote the zeal towards academic achievement among our students. The cultural, sporting and other successes of all our students and staff are also proudly celebrated together.

I congratulate the staff and students who brought latest technologies and concepts onto the day to day teaching learning platform. As long as our ideas are expressed and thoughts kindled we can be sure of learning, as everything begins with an idea.

I appreciate every student who shared the joy of participation in co-curricular and extracurricular activities along with their commitment to curriculum. That little extra we do, is the icing on the cake. 'Do more than belong – participate. Do more than care – help. Do more than believe – practice. Do more than be fair – be kind. Do more than forgive – forget. Do more than dream – work.'

With a long and rewarding history of achievement in education behind us, our DIET community continues to move forward together with confidence, pride and enthusiasm.

I hope you enjoy your visit to the website and should you wish to contact us, please find details at the www.diet.ac.in

Yours in Education

Dr.Ravi Kadiyala,

Principal

Message From HOD



Dr. S. Suresh Professor & HOD, Computer Science and Engineering

Greetings from the Department of CSE, Dhanekula Institute of Engineering & Technology, Vijayawada.!!!!

"It is a pleasure to be the head of the department of CSE. The department offers B-Tech (CSE) and M-Tech (CSE). The department has a team of highly experienced and motivated faculty members who are in process of tuning the young minds to make them globally competitive. The department is equipped with state-ofthe-art laboratories where students can enhance their knowledge and skill. The strength of the department is highly motivated students who understand the dynamics of the industry and upgrade their skills accordingly. The scope of computer science is endless. The students of the computer science and engineering are highly demanded by the recruiters of the top companies. Depending upon the interest of the student, he/she may choose to go for higher studies or if employed can choose to do research, development, design, production, application, testing or management in the Information Technology industry. In our department we not only give emphasis on study but also apply our knowledge in understanding what computers are, how to efficiently program them, different tools and technologies, the interface between the computer and the user, the computer graphics, computer networking, managing the database, software engineering and testing them efficiently and more. Through innovative teaching-learning process a teamwork approach and leadership building experience, our students gain vital communication and critical-thinking skills. Our institution provides a platform for the students to enhance their employability skills through Industry Institute Collaboration."

I, Congratulate the team of faculty members and the students for their brilliant and original efforts. I wish all the Students and Faculty a great academic career.

Dr. S. Suresh Professor & HOD,

Computer Science and Engineering,

Dhanekula Institute of Engineering & Technology, Vijayawada - 531 139.

Department Vision: To empower students of Computer Science and Engineering Department to be technologically adept, innovative, global citizens possessing human values.

Department Mission: To Encourage students to become self-motivated and problem solving individual To prepare students for professional career with academic excellence and leadership skills. To Empower the rural youth with computereducation. To Create Centre's of excellence in Computer Science and Engineer

Department PEO's:

PEO1: Excel in Professional career through knowledge in mathematics and engineering principles.

PEO2: Able to pursue higher education and research.

PEO3: Communicate effectively, recognize, and incorporate societal needs in their professional endeavors.

PEO4:Adapt to technological advancements by continuous learning

DEPARTMENT ACTIVITIES:

JNTUK issued guidelines for conducting UG/PG Project viva voce examinations online through video conferencing without compromising the quality and observing the UGC guidelines. The Head of the Department/Internal Examiner conducted as per the schedule released for the UG programmes





STUDENT ACTIVITIES:

Most of the students were actively participated in the learning activities and in doing intenships and completed and received certificates during the lockdown period.



Students are also registered for online training course on GAMIFICATION ORGANIZED BY APSSDC from 30th july to 26th August 2020



Need to remember

Upcoming courses which are offered by APSSDC APSSDC online training on Blockchain & Cloud Architecure from 10th august 2020

Do you know

GATE 2021: Application, Exam Dates (Declared),



Who is the conducting body of GATE 2021 ? - IIT Bombay

When will be the GATE 2021 exam organised ? – 5th to 7th February and 12th to 14th ebruary 2021 (Announced)

When can I register for GATE 2021? – August/ September 2020

Is GATE 2021 Syllabus or Exam pattern revised for 2021? - Yes

Are any other subjects/ papers added this year ? Yes, Environmental Science & Engineering (ES) and Humanities & Social Sciences (XS)

For how many subjects, can I appear in GATE 2021? – 1 or 2 subjects (as per the prescribed set of combinations)

Are there any changes in the eligibility criteria of GATE 2021 ? Yes

What will be the GATE exam mode – Online or Offline? – Online (CBT)

STUDENTS ARTICLES

BIOMETRIC ATM Iris Recognition

There is an urgent need for improving security in banking region. With the advent of ATM though banking became a lot easier it even became a lot vulnerable. The chances of misuse of this much hyped 'insecure' baby product (ATM) are manifold due to the exponential growth of 'intelligent' criminals day by day. ATM systems today use no more than an access card and PIN for identity verification. This situation is unfortunate since tremendous progress has been made in biometric identification techniques, including finger printing, facial recognition, and iris scanning.



This paper proposes the development of a system that integrates Iris scanning technology into the identity verification process used in ATMs. The development of such a system would serve to protect customers and financial institutions alike from fraud and other breaches of security.

Keywords: Biometric, ATM, PIN, Security, Fingerprint, Encryption

Thus, we are looking forward to an ATM model that is more reliable in providing security by using iris scanner software. By keeping the time elapsed in the verification process to a negligible amount we even try to maintain the efficiency of this ATM system to a greater degree. Furthermore, since nearly all ATMs videotape customers engaging in transactions, it is no broad leap to realize that banks already build an archive of their customer images, even if they are not necessarily grouped with account information.



Md.Rehana

Roll.No:168T1A0561

FERROELECTRIC RAM

Ferroelectric memory is a new type of semiconductor memory, which exhibit short programming time, low power consumption and nonvolatile memory, making highly suitable for application like contact less smart card, digital cameras which demands many memory write operations.

A ferroelectric memory technology consists of a complementary metal- oxide-semiconductor (CMOS) technology with added layers on top for ferroelectric capacitors. A ferroelectric memory cell has at least one ferroelectric capacitor to store the binary data, and one transistor that provide access to the capacitor or amplify its content for a read operation. Once a cell is accessed for a read operation, its data are presented in the form of an analog signal to a sense amplifier, where they are compared against a reference voltage to determine their logic level



Ferroelectric memories have borrowed many circuit techniques (such as folded-bitline architecture) from DRAM's due to similarities of their cells and DRAM's maturity. Some architectures are reviewed here.

Ferroelectric memory exhibit short programming time, low power consumption and nonvolatile memory, making highly suitable for application like contact less smart card, digital cameras which demanding many memory write operations. In other word FRAM has the feature of both RAM and ROM. A ferroelectric memory technology consists of a complementry metal-oxide- semiconductor (CMOS) technology with added layers on top for ferroelectric capacitors. A ferroelectric memory cell has at least one

ferroelectric capacitor to store the binary data, and one or two transistors that provide access to the capacitor or amplify its content for a read operation.

A ferroelectric capacitor is different from a regular capacitor in that it substitutes the dielectric with a ferroelectric material (lead zirconate titanate (PZT) is a common material used)-when an electric field is applied and the charges displace from their original position spontaneous polarization occurs and displacement becomes evident in the crystal structure of the material. Importantly, the displacement does not disappear in the absence of the electric field. Moreover, the direction of polarization can be reversed or reoriented by applying an appropriate electric field.



krishna sai

Roll No:178T1A0525

GI-FI TECHNOLOGY



LOGO OF Gi-Fi

Gi-Fi or Gigabyte wireless refers to a wireless communication at a data range of more than one billion bits per second. It is a worlds first transceiver integrated on a single chip. Gi-Fi will allow wireless transfer of audio and video data upto 5 gigabyte per second, ten times the current maximum wireless transfer rate, at one-tenth of the cost.

WHY ONLY Gi-Fi THAN Wi-Fi AND Li-Fi: Gi-Fi is a latest technology its speed is 5 Gbps, It has high range density and it is low cost when compared with Wi-Fi and Li-Fi.

Gi-Fi CHIP:



Gi-Fi utilizes a 5mm square chip and a 1mm wide antenna burning less than 2milli watts of power to transmit data wirelessly over short distances. The Gi-Fi chip is a good for personal area networking because there is no internet infrastructure available to cop it with. It can have a span of 10 meters. With the help of gifi chips the videos sharing can be possible without any hurdles. The Gi-Fi chip is one of Australia's most lucrative technologies.

FEATURES OF Gi-Fi:

Gi-Fi technology provides many features such as ease of deployment, small form factor, enabling the future of information management, high speed of data transfer, low power consumption etc.

Gi-Fi allows a full-length high definition movie to be transferred between two devices in seconds.

ADVANTAGES OF GI-FI:

First most is there is no need of any cable wires, Chip is very low cost, Small size, High portable, High mobility and so on...

GI - FI TECHNOLOGY



Finally today's world technology is moving towards the Gi-Fi technology, where it is a wireless communication which can transfer data with more mobility and it is small in size.

Thank you



Y.Josthsna

Roll.No:188T1A0560

Art gallery







Pencil art by A.Bhuvana, Rollno:198t1a0508



This is to inform to all the II/IV, III/IV and IV/IV BTech., students that the class work through ON LINE mode will be commencing from August 17th 2020.the students are informed to be ready for the classwork This is for your information and necessary preparation. "

Thank You





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